

REMARKS

Applicants thank the Examiner for the thorough consideration given the present application. Claims 1, 2, 6, and 7 are currently being prosecuted. The Examiner is respectfully requested to reconsider his rejections in view of the amendments and remarks as set forth below.

Interview Summary

Applicants note with appreciation the personal interview conducted with Examiner Shapiro on November 21, 2003. At that interview, the amended claims were discussed in regard to the rejections. No agreement was reached at that interview.

Entry of Amendment

It is respectfully requested that the present Amendment should be entered in the official file in view of the fact that the amendment automatically places the application in condition for allowance. Furthermore, Applicants submit that the Amendment should be entered since no new issues are being presented. The major change to claims 1 and 6 is the inclusion of several lines which were previously presented in cancelled claims 4 and 8. Accordingly, these changes do not involve any new issues.

Paragraph (f) of claims 1 and 6 has also been changed to refer to "STV1" rather than "STV". This also does not present a new issue since this is merely a slight narrowing of the claim to refer to only one of the two STV signals. This is not considered to be a new issue since the pausing of the signal was previously considered. The only other change to the claims is in paragraph (a) of claim 6 to include the vertical blank period. This has only been

inserted to provide antecedent basis for its recitation in paragraph (f). In addition, this limitation was previously found in claim 1. Accordingly, no new issues are being presented in this Amendment. Accordingly, entry of the Amendment and full consideration is considered to be proper.

Rejection Under 35 U.S.C. § 112

Claims 1, 2, 4 and 6-8 stand rejected under 35 U.S.C. § 112, first paragraph as containing subject matter not described in the specification. This rejection is respectfully traversed.

Applicants submit that this description is found in the originally filed application on page 5, lines 11-13 and on page 6, lines 9-11. Accordingly, Applicants submit that this rejection should be removed.

Rejection Under 35 U.S.C. § 103

Claims 1, 2, 4 and 6-8 stand rejected under 35 U.S.C. § 103 as being obvious over the admitted prior art of the present application. This rejection is respectfully traversed.

First, it is noted that claims 4 and 8 have been cancelled rendering this part of the rejection moot.

The Examiner states that the admitted prior art teaches a method of processing signals including receiving a vertical synchronizing signal, receiving a data enable signal, generating a gate clock signal, after an edge of the vertical synchronizing signal generating gate-on enable signals and start vertical signals. The Examiner admits that the admitted prior art does not show pausing the output of the signals until the end of the vertical blank

period. The Examiner feels that it would have been obvious to pause the output of the signals until the end of the vertical blank period.

Applicants submit that the amended claims could not be obvious over the admitted prior art. As explained in the interview, and as shown in Figures 7 and 8, signals CPV and OE are present at the edge of the VSYNC signal and continue during the vertical blanking period. At some point during the vertical blanking period, these signals stop or are paused. Likewise, signal STV1 occurs during the vertical blanking period and then is likewise paused. Accordingly, there is a pause in these three signals that starts during the vertical blanking period and ends shortly after the blanking period ends. A similar arrangement is shown in Figure 8. This arrangement is designed to allow the control signals to be generated in real time rather than based on values from the previous cycle.

As shown in the prior art Figures, this arrangement is not present and it would not be obvious to make these changes without the realization that it is possible to control the values in real time. There is no teaching in the admitted prior art of the pausing of these signals nor the need to do so. Furthermore, the prior art teaches using the values from the previous cycle, which causes the display frame to jitter or bounce.

Both claims 1 and 6 describe the edge of the vertical synchronizing signal and generation of start vertical signals before the end of the vertical blank period and after at least a gate clock cycle during the vertical blank period and then pausing the CPV, STV1 and OE signals until the end of the vertical blank period. This concept would not be obvious over the admitted prior art since there is no motivation for one skilled in the art to make these changes without the realization that real time control can be accomplished.

The result of utilizing this arrangement is that the jitter and bounce of the prior art is avoided.

Conclusion

In view of the above remarks, it is believed that the claims clearly distinguish over the patents relied on by the Examiner, either alone or in combination. In view of this, reconsideration of the rejection and allowance of all the claims are respectfully requested.

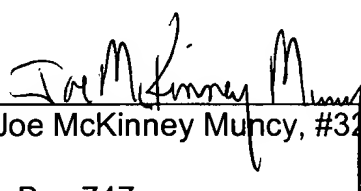
Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Robert F. Gnuse (Reg. No. 27,295) at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

Pursuant to 37 C.F.R. §§ 1.17 and 1.136(a), Applicants respectfully petition for a one (1) month extension of time for filing a response in connection with the present application and the required fee of \$110.00 is being filed concurrently herewith.


If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

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